

AMENDMENTS TO THE SPECIFICATION

Immediately following the title, please add the following:

--CROSS REFERENCE TO RELATED APPLICATIONS

This application is a national phase patent application of PCT/GB04/03670 filed on August 27, 2004 which claims priority to United Kingdom Patent No. 0320167.0 filed on August 28, 2003.--

On page 1, Line 5, please insert: -- Background of the Invention—

On page 7 line 1 of the Specification please amend the first paragraph as follows:

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3/16/2011

--Two inverse-phased sin-wave voltage inputs are buffered by OPA 602 buffers and then connected to the non-inverting input of each AD844, where two capacitances C connected with the inverting inputs 2 of the OPA 602 buffers and two resistances r connected with the output 6 of a first AD844 chip can perfectly restore the Direct-Current (DC) components generated in the circuit and then cancel the DC offset at the current outputs. Eight AD844s constitute 4 pairs of two-output VCCSs.

Two inverting inputs of each pair of AD844s are cascade-connected together with a current- setting resistor R. The positive and negative current outputs of each pair are summed together to form larger current outputs. The total output currents can be estimated using the following function:--

On page 8, beginning with line 30 please amend the paragraph as follows:

--The coupling circuits, as shown in Figure 10, are made of a number of voltage buffers IC1 and passive components C1 R1 R2 R3 to provide a wide bandwidth and fast settling ac-coupling interface, as well as a dc bypass to the dc current bias of the programmable gain amplifier (PGA). The input can be configured in either the single or the differential input mode. Only the differential mode is illustrated in Figure 10 for the use of the adjacent measurement strategy in electrical impedance tomography, where  $R1=R3$  and  $R1$  is much smaller than  $R2$ . Therefore,  $R2$ ,  $C1$  dominate the bandwidth of the coupling,  $R1$  and  $R3$  provide a DC bypass to the DC current offset of PGA but the responding speed is fast since the transient time is dominated by  $R1$  or  $R3$ .--